

Fig. 1

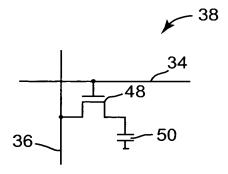


Fig. 2

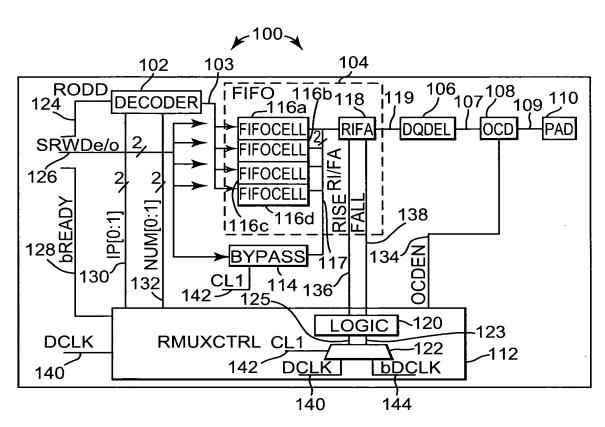


Fig. 3

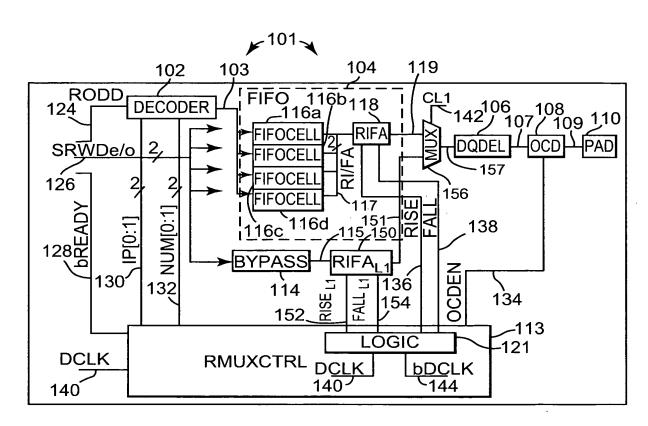


Fig. 4

## Docket No. 2003P52601US/I331.102.101 RANDOM ACCESS MEMORY WITH OPTIONAL COLUMN ADDRESS STROBE LATENCY OF ONE Torsten Partsch

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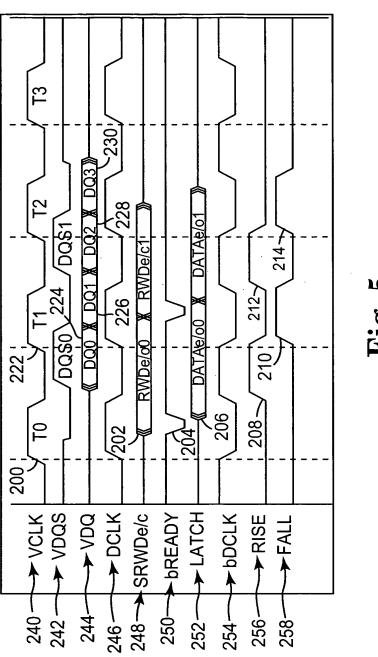


Fig. 5